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the largest empty rectangle problem for the case of points within rectangle an optimal algorithm of time complexity is  $O(n \log n)$ . Naamad, D. T. Lee and W

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The study of arrangements was begun by Jakob Steiner, who proved the first bounds on the maximum number of features of different types that an arrangement may have. [3]

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In this paper several methods for implementing the multidimensional Fourier transform together with the VLSI An optimal architecture Chazelle, B.;

### **Voronoi diagrams in $L_1$ metrics**

Metrics with 2-Dimensional Storage Applications. An optimal (worst-case B. Chazelle, R. L. Drysdale, and D. T. Lee.

### **Optimal tradeoffs for addition on systolic arrays**

J. L. Carter and S. R. Kosaraju, "Optimal Tradeoffs for Addition on Systolic Arrays," Manuscript, 1986. B.M. Chazelle and L. Monier, "Optimality in VLSI," Proc.

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mented in one (or a few) VLSI chips. Optimal measures for silicon area and processing time are primary concerns. Chazelle, B. and L Monier.

### **Almost optimal set covers in finite VC-dimension -**

Almost Optimal Set Covers in Finite VC-dimension This problem is motivated by VLSI design criteria as B. Chazelle. An optimal convex hull algorithm

### **Intersection of convex objects in two and three**

B. CHAZELLE Yale University, New Haven, Connecticut AND D. P. DOBKIN Princeton University, Princeton, New Jersey Abstract. One of the basic geometric operations

### **Optimal search in planar subdivisions : siam**

Orthogonal drawings of graphs for the automation of VLSI Bernard Chazelle. (1992) An Optimal Algorithm for SIAM Journal on Computing 21

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Chazelle, B.; on the area of a VLSI layout when drivers are introduced of the traveling salesman problem has a uniquely optimal solution is complete

### **Tight chip area lower bounds for discrete fourier**

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Hussein M. Alnuweiri , V. K. Prasanna Kumar, Optimal VLSI Sorting with Reduced Number of Processors, B. Maggs , B. Chazelle, D. P. Dobkin:

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### **Patent us5553206 - method and system for producing**

Method and system for producing mesh representations of objects an optimal tradeoff between a reduced number of mesh elements and the overall B. Chazelle

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List of computer science publications by Bernard Chazelle. default search action. combined dblp search; Bernard Chazelle: An Optimal Convex Hull Algorithm in Any

### **A vlsi network for variable size fft's**

A VLSI Network for Variable Size FFT's. B. Chazelle: Pages: 697-707: Although the problem of the construction of optimal k-processor schedules for unit time

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Baker 508 12 SIAM J. Computing 1983 197 B. CHAZELLE (1983) (1983) An algorithm for optimal two-dimensional compaction of VLSI layouts. VLSI J.1, 179-209.

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The Design and Analysis of Parallel Algorithms. Uploaded by Muhamad Arvie Pagama. Info; Research Interests: Genetic Algorithms

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Miranda's part in this study is finding optimal , report of the Computational Geometry Impact Task Force convened by B. Chazelle. This VLSI layout

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time- and VLSI-optimal, algorithm for convex hull computation on meshes with multiple broadcas IL (1993) III, 196-201. [9] B. Chazelle,

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Approximation Schemes for Partitioning: Convex VLSI, Robotics, Graphics Bernard Chazelle and David P. Dobkin. Optimal convex decompositions.

### **Euclidean shortest paths in the presence of**

Euclidean shortest paths in the presence of rectilinear barriers. Networks, 14: 393 transformation for planar VLSI B. Chazelle, H . Edelsbrunner, M

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Computing Homotopic Shortest Paths in the Plane sic VLSI problem, B. Chazelle. Cutting hyperplanes

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Carnegie Mellon University Research Showcase @ CMU Computer Science Department School of Computer Science 1981 Optimality in VLSI Chazelle Carnegie Mellon University

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A new model of computation for VLSI, {Bernard Chazelle and Louis Monier} Area-time optimal VLSI networks based on the cubeconnected cycles - PREPARATA,

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we discussed collision detection in general and surveyed some This was a big problem in the 1970's and early 1980's in VLSI B. Chazelle, (1988) A

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